X-1056 US 10/737,386.

IN THE UNITED STATES PATENT OFFICE

PATENT Conf. No.: 5383

Applicants:

Brian T. Brunn

Assignee:

Xilinx, Inc.

Title:

"Adaptive Transition Density Data Triggered PLL (Phase Locked Loop)"

Serial No.:

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File Date:

December 16, 2003

Examiner:

Aslan Ettehadieh

Art Unit:

2611

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Mail Stop AMENDMENT

**COMMISSIONER FOR PATENTS** 

P.O. Box 1450

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## AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

## Dear Examiner:

In response to the First Office Action mailed from the Patent Office on May 14, 2007, please enter the following amendments and entertain the following remarks.

Amendments to the Claims begin on page 2 of this paper.

**Remarks** begin on page 8 of this paper.